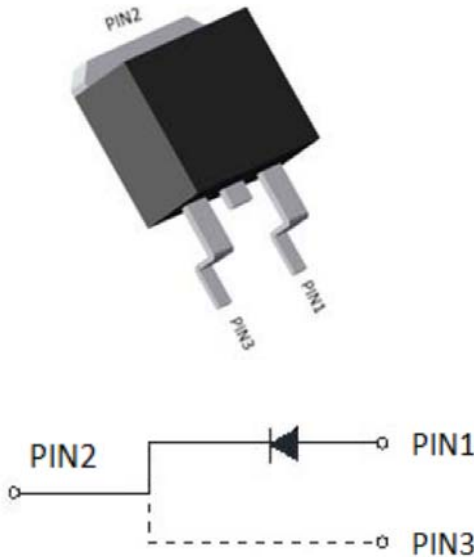


Silicon Carbide Schottky Diode

| | |
|---------------|---------|
| V_{RRM} | 650 V |
| I_F (135°C) | 50 A |
| Q_C | 135.3nC |



Features

- Positive temperature coefficient
- Temperature-independent switching
- Maximum working temperature at 175 °C
- Unipolar devices and zero reverse recovery current
- Zero reverse recovery current
- Essentially no switching losses
- Reduction of heat sink requirements
- High-frequency operation
- Reduction of EMI

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO-263
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | VALUE |
|--|----------------|----------------------|-------------|
| Device marking code | | | D106520BQG3 |
| Reverse voltage (repetitive peak) @ $T_j=25^\circ\text{C}$ | V_{RRM} | V | 650 |
| Reverse voltage (Surge Peak) @ $T_j=25^\circ\text{C}$ | V_{RSM} | V | 650 |
| Reverse voltage (DC) @ $T_j=25^\circ\text{C}$ | V_{DC} | V | 650 |
| Continuous forward current @ $T_c=25^\circ\text{C}$ | I_F | A | 108 |
| Continuous forward current @ $T_c=135^\circ\text{C}$ | | | 50 |
| Continuous forward current @ $T_c=160^\circ\text{C}$ | | | 20 |
| Non-repetitive peak forward surge current @ $T_c=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Wave | I_{FSM} | A | 380 |
| Power Dissipation@ $T_c=25^\circ\text{C}$ | P_{TOT} | W | 375 |
| Power Dissipation@ $T_c=110^\circ\text{C}$ | | | 162.5 |
| i^2t Value@ $T_c=25^\circ\text{C}$, $t_p=10\text{ms}$ | $\int i^2 dt$ | A^2S | 722 |
| Operating junction and Storage temperature range | T_j, T_{stg} | $^\circ\text{C}$ | -55 to +175 |



■Electrical Characteristics

| PARAMETER | SYMBOL | UNIT | TEST CONDITIONS | Typ. | Max. |
|---------------------------|--------|---------|--|-------|------|
| Forward voltage drop | V_F | V | $I_F=20A, T_J=25^{\circ}C$ | 1.2 | 1.3 |
| | | | $I_F=20A, T_J=175^{\circ}C$ | 1.27 | - |
| Reverse leakage current | I_R | μA | $V_R=650V, T_J=25^{\circ}C$ | 3 | 25 |
| | | | $V_R=650V, T_J=175^{\circ}C$ | 20 | - |
| Total capacitive charge | Q_C | nC | $V_R=400V, T_J=25^{\circ}C, Q_C=\int_0^{V_R} I_R(V)dV$ | 135.3 | - |
| Total capacitance | C | pF | $V_R=0V, f=1MHZ$ | 2453 | - |
| | | | $V_R=200V, f=1MHZ$ | 247 | - |
| | | | $V_R=400V, f=1MHZ$ | 243 | - |
| Capacitance Stored Energy | E_C | μJ | $V_R=400V$ | 16.5 | - |

■Thermal Characteristics ($T_a=25^{\circ}C$ Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | Value |
|--------------------|------------------|---------------|-------|
| Thermal resistance | $R_{\theta J-C}$ | $^{\circ}C/W$ | 0.4 |

■Typical Characteristics

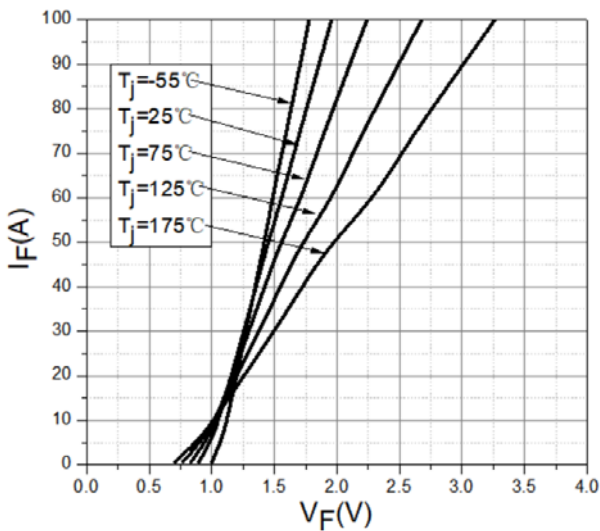


Figure 1. Forward Characteristics

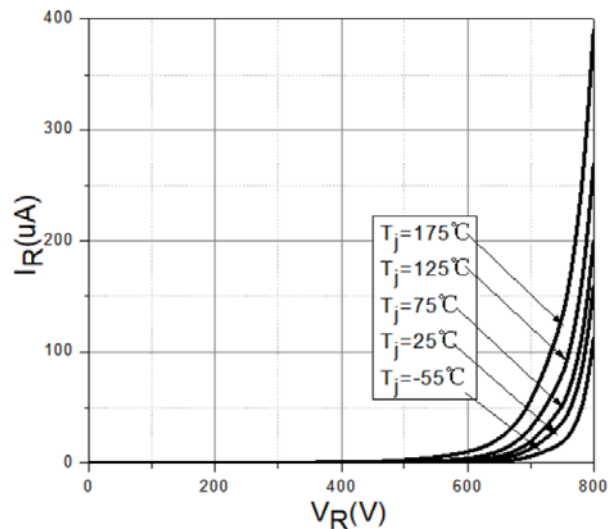


Figure2. Reverse Characteristic

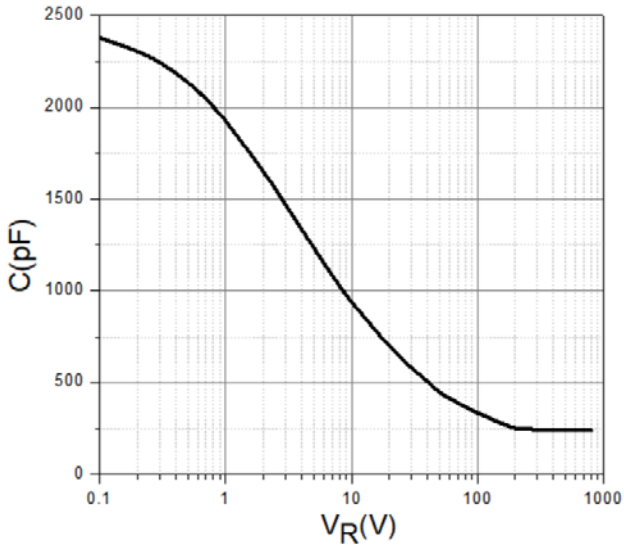


Figure 3. Capacitance vs. Reverse Voltage

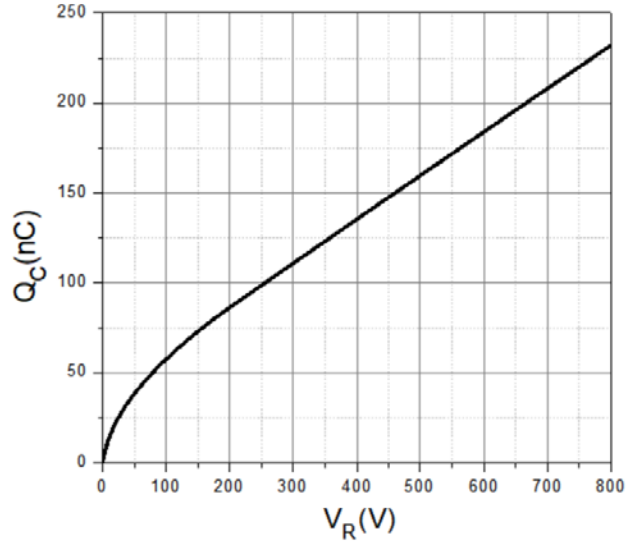


Figure 4. Total Capacitance Charge vs. Reverse Voltage

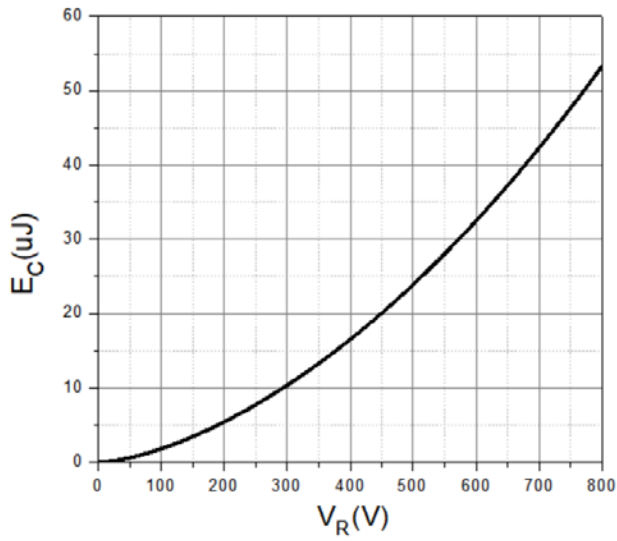


Figure 5. Capacitance Stored Energy

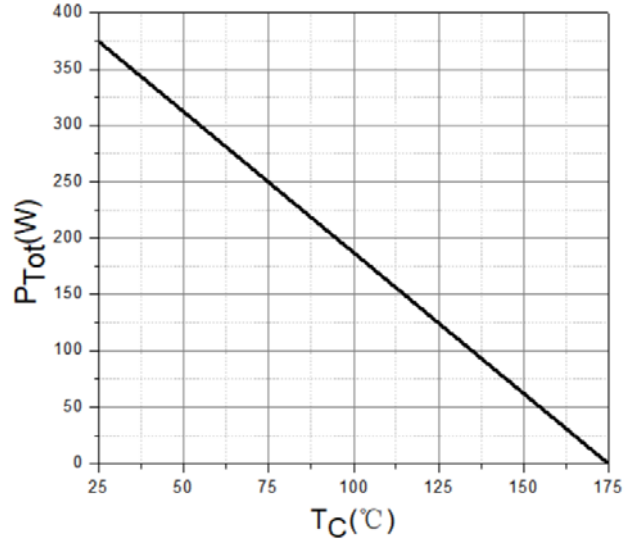


Figure 6. Power Derating

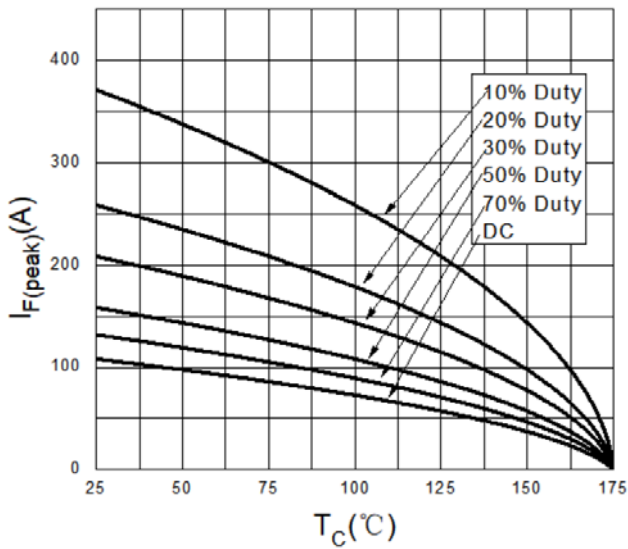


Figure 7. Current Derating

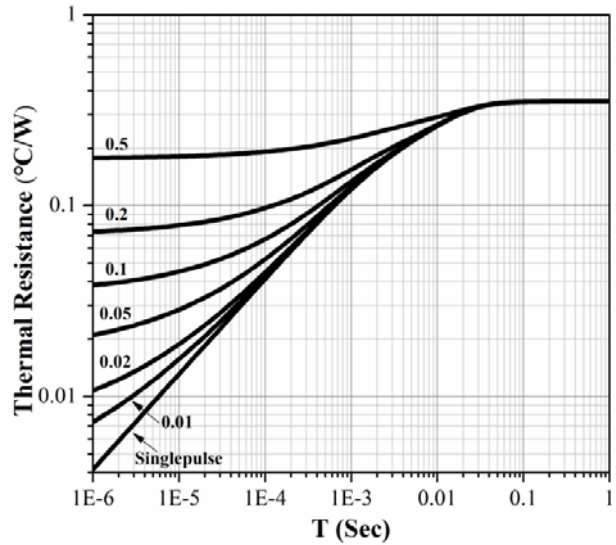
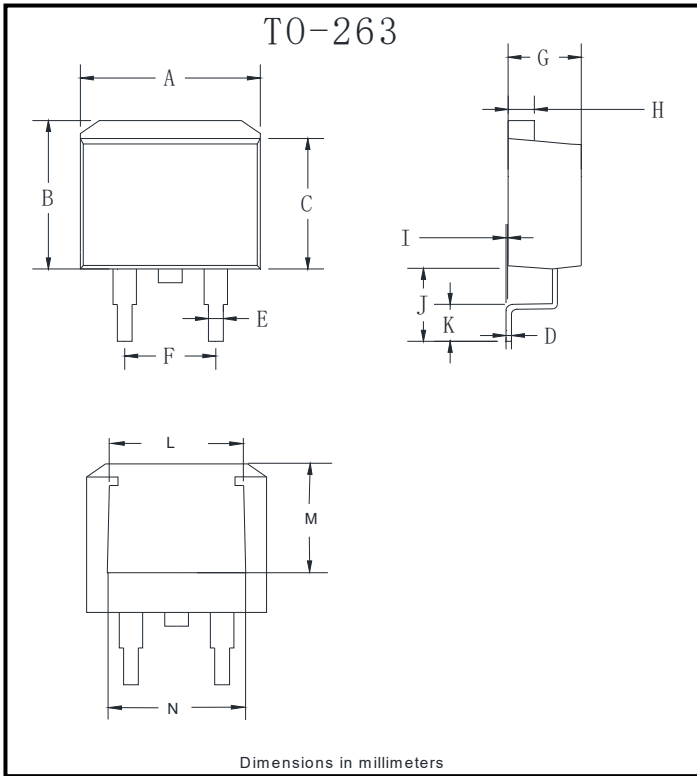


Figure 8. Transient Thermal Impedance



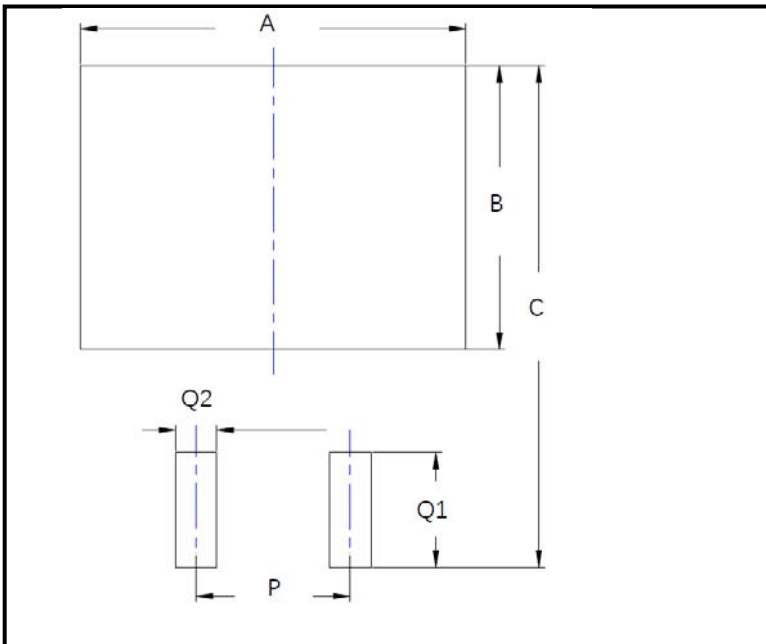
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■ Outline Dimensions



| TO-263 | | |
|--------|------|------|
| Dim | Min | Max |
| A | 9.5 | 11.5 |
| B | 9.7 | 10.5 |
| C | 8.4 | 9.0 |
| D | 0.28 | 0.64 |
| E | 0.68 | 0.94 |
| F | 4.55 | 5.6 |
| G | 4.04 | 5.10 |
| H | 1.14 | 1.4 |
| I | 0 | 0.2 |
| J | 4.9 | 6.05 |
| K | 1.79 | 2.79 |
| L | 7.3 | 7.9 |
| M | 6.2 | 6.8 |
| N | 7.6 | 8.2 |

■ Suggested Pad Layout



| Dim | Millimeters |
|-----|-------------|
| A | 12.7 |
| B | 9.4 |
| C | 16.6 |
| P | 5.08 |
| Q1 | 3.8 |
| Q2 | 1.35 |



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Disclaimer

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