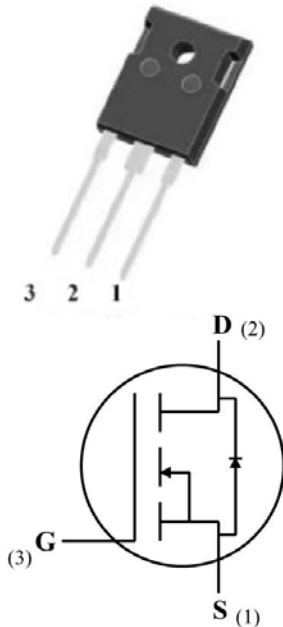


## Silicon Carbide Power MOSFET (N-Channel Enhancement)

$V_{DS}$	1200V
$I_D$ (25°C)	79A
$R_{DS(on)}$	40mΩ



### Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant
- AEC-Q101 qualified

### Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

### Mechanical Data

- **Package:** TO-247AB
- **Terminals:** Tin plated leads
- **Polarity:** As marked

### ■Maximum Ratings ( $T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D212040NCTGH2		
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	1200	$V_{GS}=0\text{ V}, I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-10/+25	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-4/+18	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	$I_D$	A	79	$V_{GS}=18\text{V}, T_c=25^\circ\text{C}$	Fig.14
Continuous drain current @ $T_c=110^\circ\text{C}$			54	$V_{GS}=18\text{V}, T_c=110^\circ\text{C}$	
Pulsed drain current	$I_{D(pulsed)}$	A	360	Pulse width $t_p$ limited by $T_{j,max}$	Fig.15
Avalanche energy, Single Pulse	$E_{AS}$	mJ	800	$V_{DS}=75\text{V}, L=30\text{mH}$	
Power Dissipation	$P_{TOT}$	W	375	$T_c=25^\circ\text{C}, T_j = 175^\circ\text{C}$	Fig.13
Operating junction and Storage temperature range	$T_j, T_{stg}$	$^\circ\text{C}$	-55 to +175		
Soldering temperature	$T_L$	$^\circ\text{C}$	260	1.6mm (0.063") from case for 10s	
Mounting torque	$T_M$	Nm	1.0	M3 screw Maximum of mounting process: 3	



### ■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	1.5	2.9	4.0	$V_{DS}=V_{GS}, I_D=50mA$	Fig.4, 11
Drain source breakdown voltage	$V_{(BR)DSS}$	V	1200			$V_{GS}=0V, I_D=100\mu A$	
Zero gate voltage drain current	$I_{DSS}$	uA		<1	50	$V_{DS}=1200V, V_{GS}=0V$	
				10	500	$V_{DS}=1200V, V_{GS}=0V, T_j=175^\circ C$	
Gate source leakage current	$I_{GSS}$	nA			250	$V_{GS}=18V, V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS(on)}$	mΩ		40	45	$V_{GS}=18V, I_D=40A$	Fig.3, 5, 6
				60		$V_{GS}=18V, I_D=40A, T_j=175^\circ C$	
Transconductance	$g_f$	S		17		$V_{DS}=20V, I_D=40A$	

### ■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	$C_{iss}$	pF		4909		$V_{DS}=800V, V_{GS}=0V, T_j=25^\circ C,$ $f=1MHz, V_{AC}=25mV$	Fig.10
Output capacitance	$C_{oss}$			198			
Reverse capacitance	$C_{rss}$			34			
Coss stored energy	$E_{oss}$	uJ		80.5			Fig.12
Gate source charge	$Q_{gs}$	nC		91		$V_{DS}=800V, V_{GS}=-4/+18V, I_D=40A$	Fig.16
Gate drain charge	$Q_{gd}$			88			
Gate charge	$Q_g$			292			
Internal Gate Resistance	$R_{G(int)}$	Ω		1.0	5.0	$f=1MHz, V_{AC}=25mV$	

### ■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		31.5		$V_{DD}=800V, V_{GS}=-4/+18V, I_D=40A,$ $L=100\mu H, R_{G(ext)}=2.7\Omega$	Fig.17, 18
Rise time	$t_r$			51.2			
Turn off delay time	$t_{d(off)}$			42			
Fall time	$t_f$			21			
Turn on switching energy	$E_{on}$	uJ		1373			
Turn off switching energy	$E_{off}$			380			



## ■Body diode characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	$V_{SD}$	V		4.0		$V_{GS}=0V, I_{SD}=20A$	Fig.8
Continuous diode forward current	$I_S$	A		60		$V_{GS}=0V, T_c=25^\circ C$	
Reverse recovery time	$t_{rr}$	nS		79		$V_{DS}=800V, V_{GS}=0V, I_{SD}=20A, di/dt=300A/\mu S$	
Reverse recovery charge	$Q_{rr}$	nC		284			
Peak reverse recovery current	$I_{rrm}$	A		6.8			

## ■Thermal Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	$R_{\theta J-C}$	$^\circ C/W$	0.4

## ■Typical Characteristics

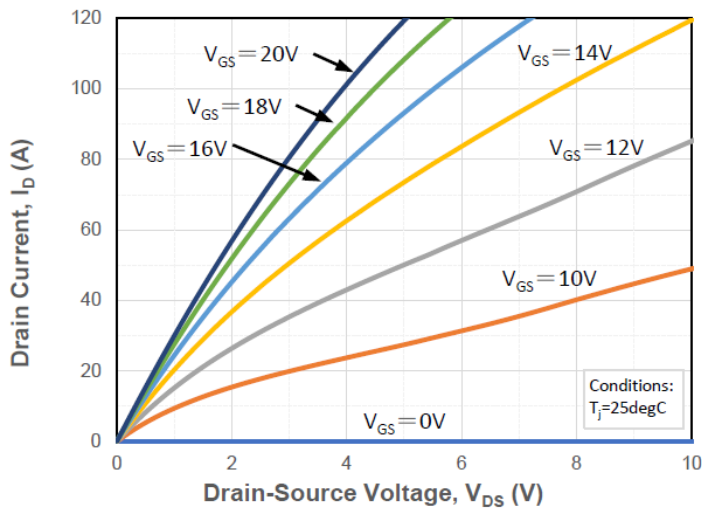


Figure 1. Output Characteristics Tj = 25°C

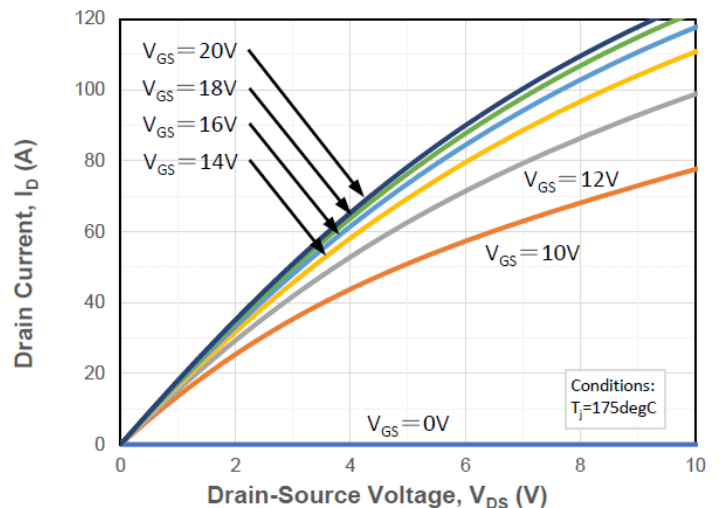


Figure 2. Output Characteristics Tj = 175°C

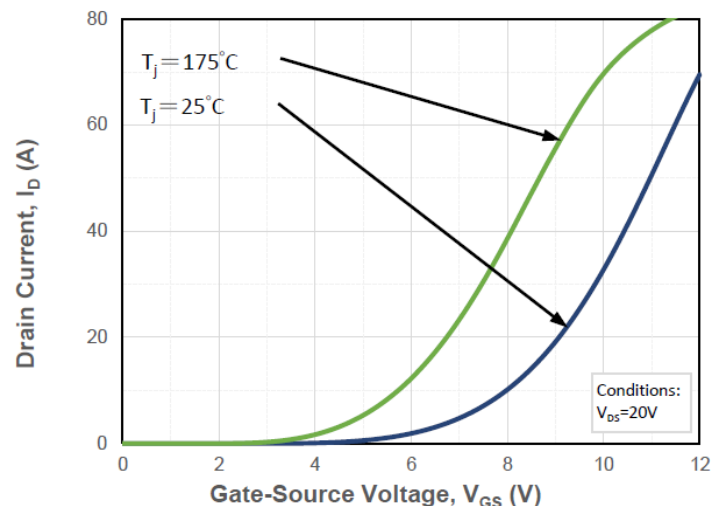
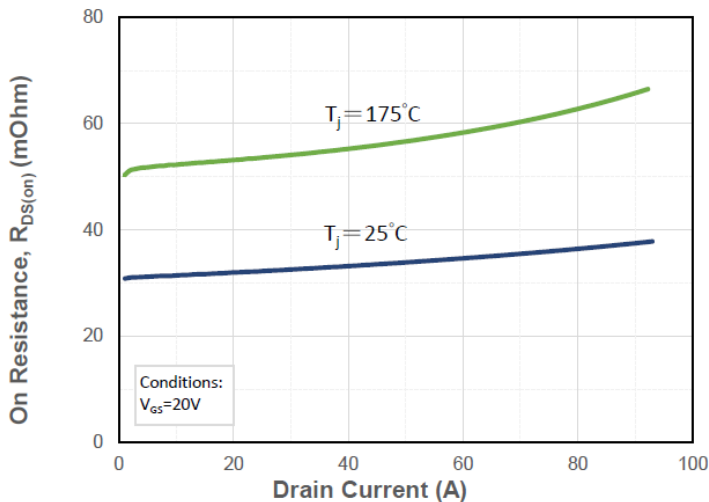




Figure 3. On-resistance vs. drain current

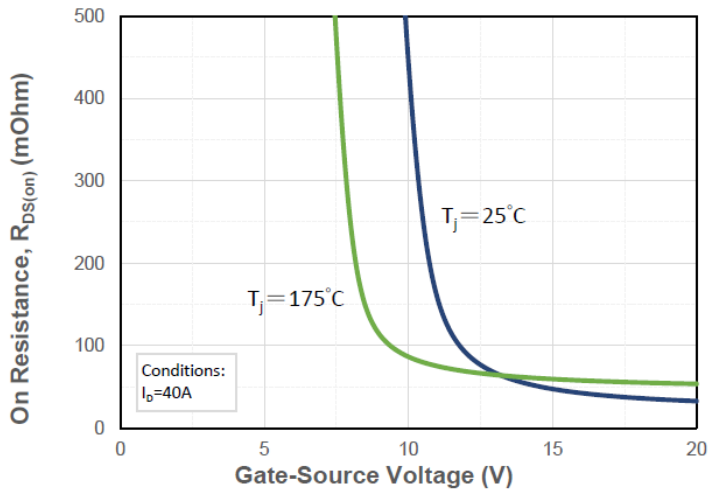


Figure 4. Transfer Characteristics for various  $T_j$

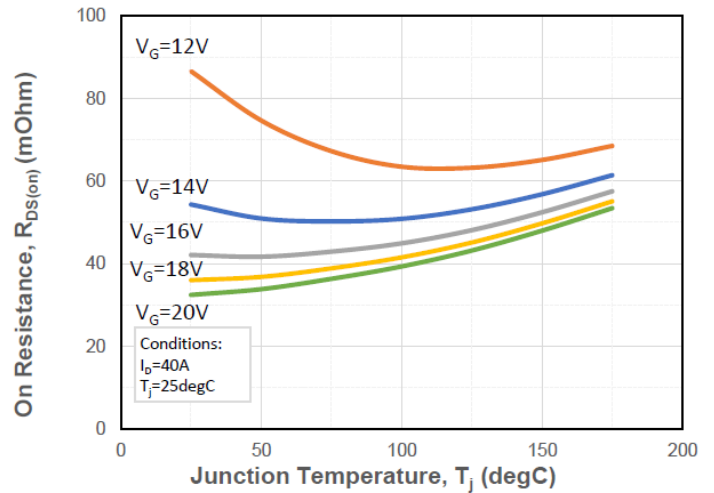


Figure 5. On-resistance vs. gate voltage for various  $T_j$

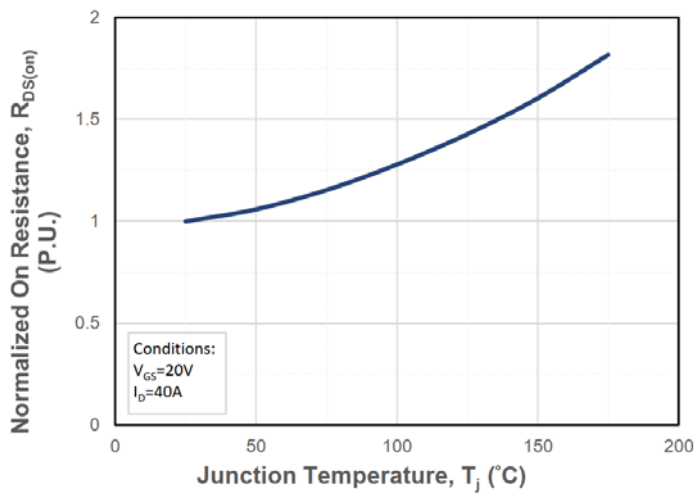


Figure 6. On-resistance vs. Temperature for various Gate voltage

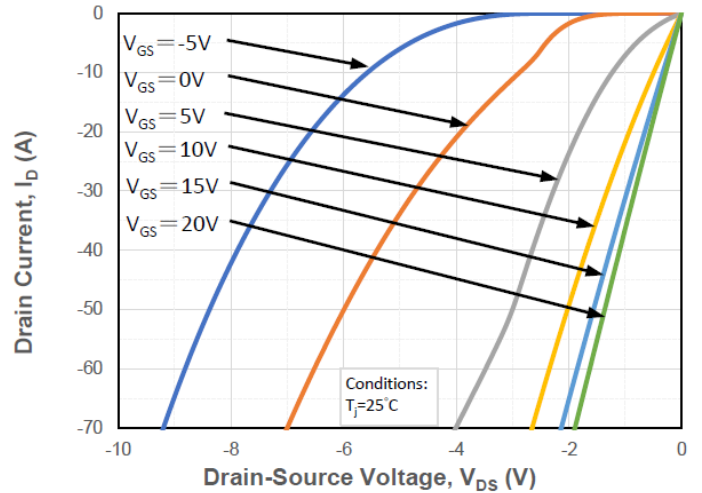


Figure 7. Normalized On-Resistance vs. Temperature

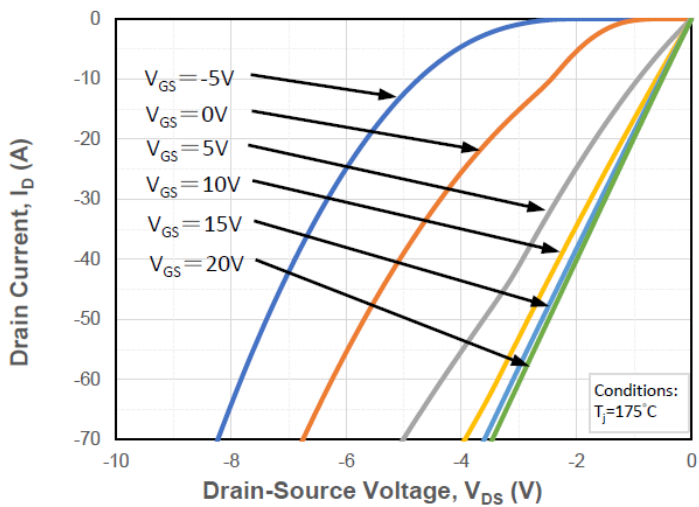


Figure 8. Reverse Output Characteristics at  $T_j = 25^\circ\text{C}$

Figure 9. Reverse Output Characteristics at  $T_j = 175^\circ\text{C}$

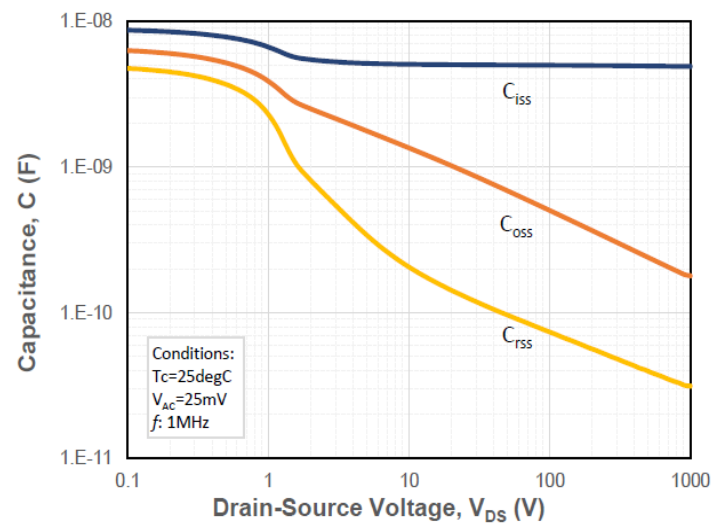


Figure 10. Capacitances vs. Drain to Source Voltage



# YJD212040NCTGH2Q

RoHS  
COMPLIANT

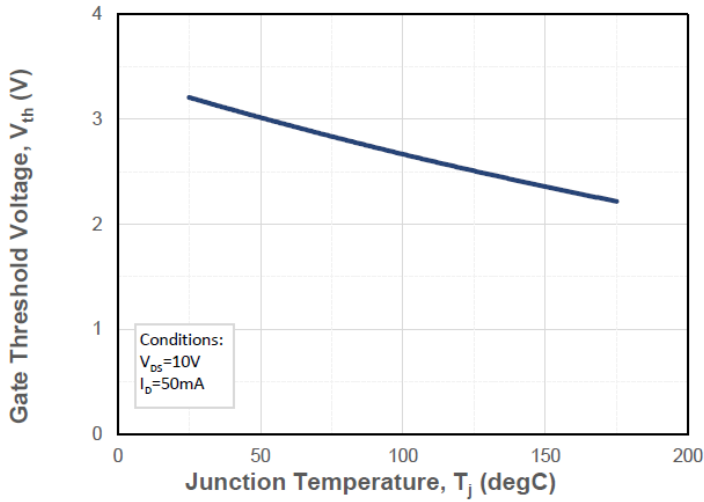


Figure 11. Threshold voltage vs. temperature

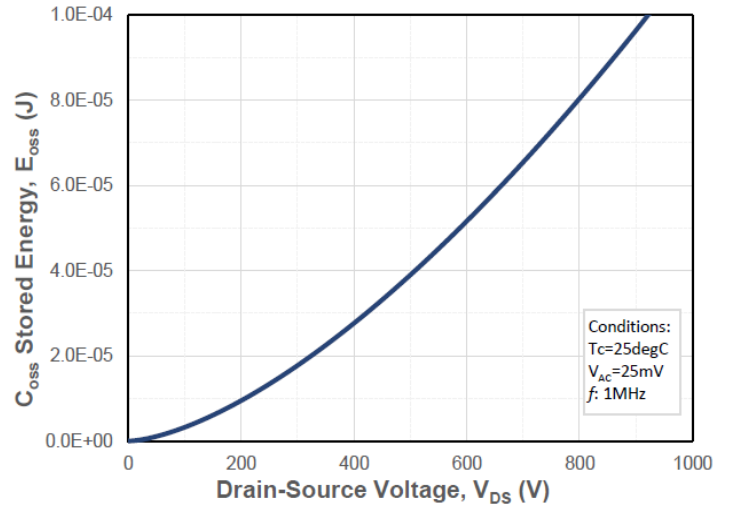


Figure 12. Output Capacitor Stored Energy

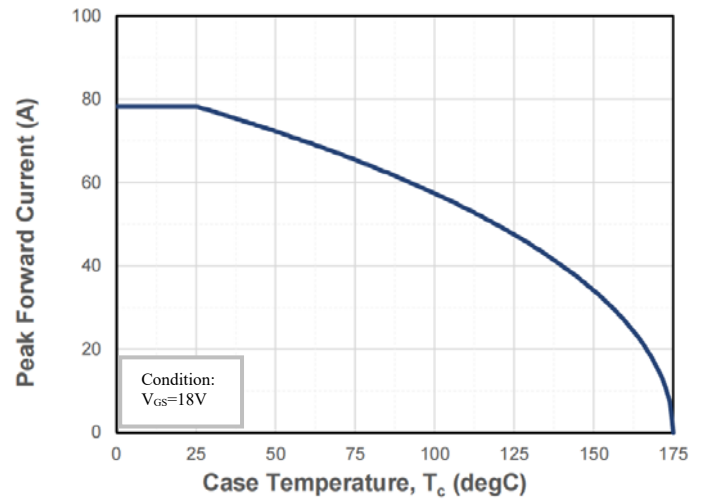
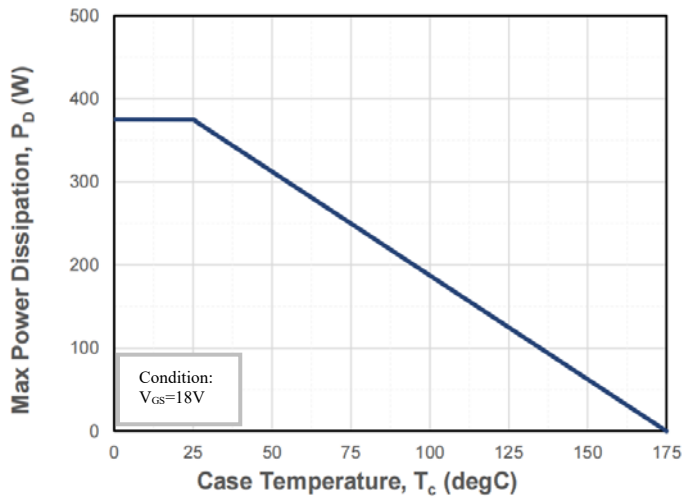


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

Figure 14. Drain Current Derating vs. Case Temperature

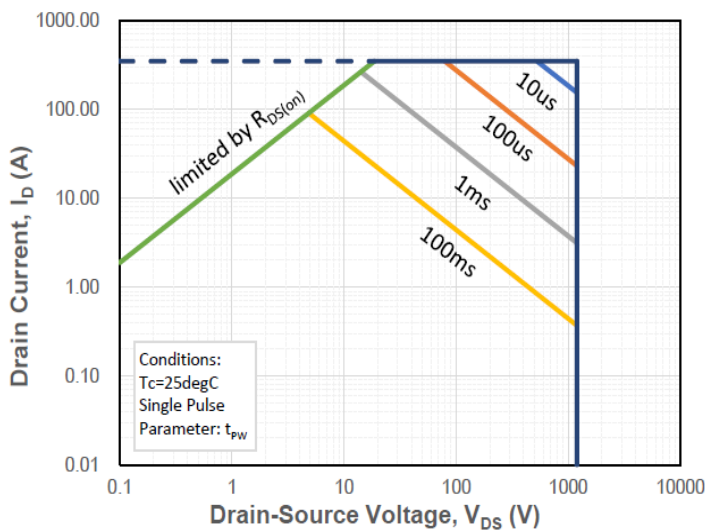


Figure 15. Safe Operating Area

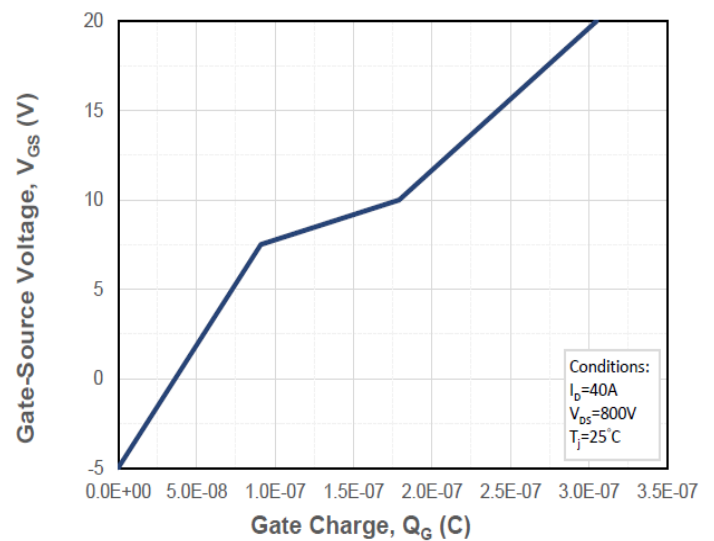


Figure 16. Gate Charge Characteristics

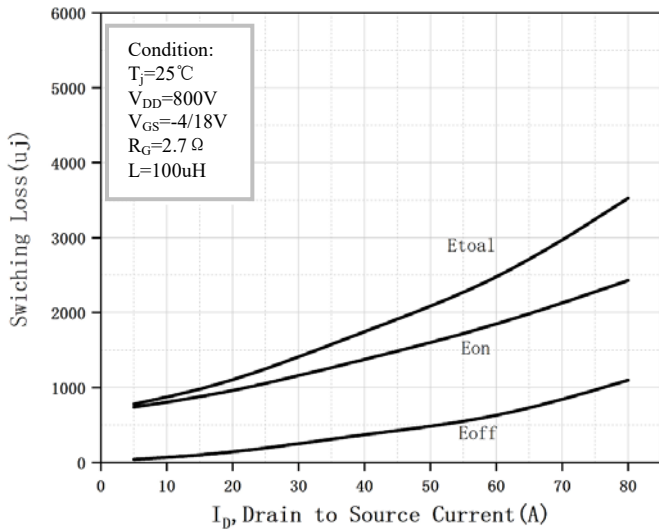


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

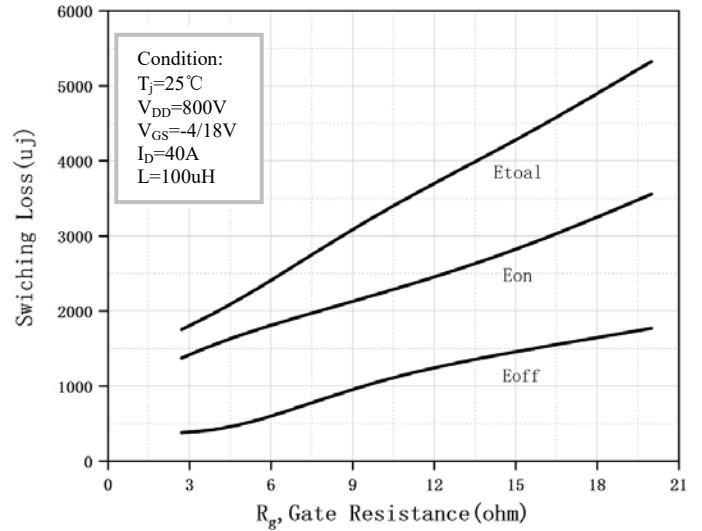


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor ( $R_{G(ext.)}$ )

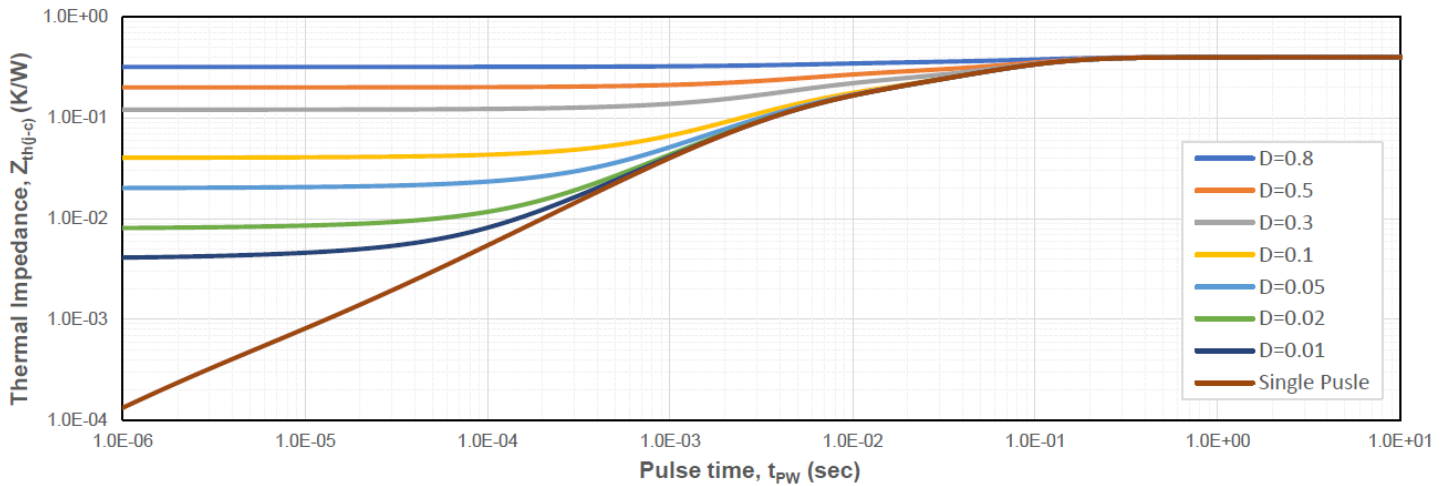


Figure 19. Transient Junction to Case Thermal Impedance

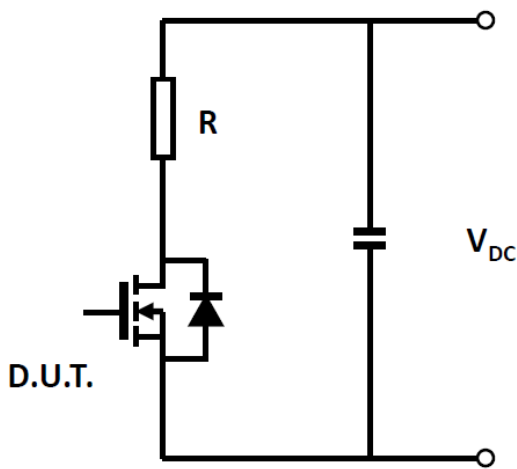


Figure 20. Schematic of Resistive Switching

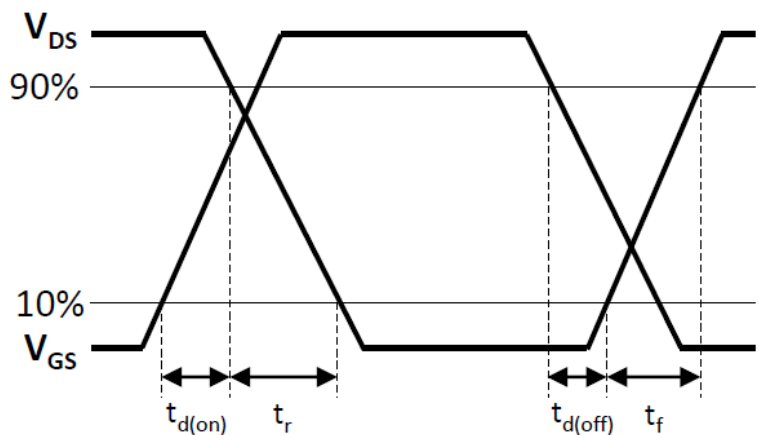
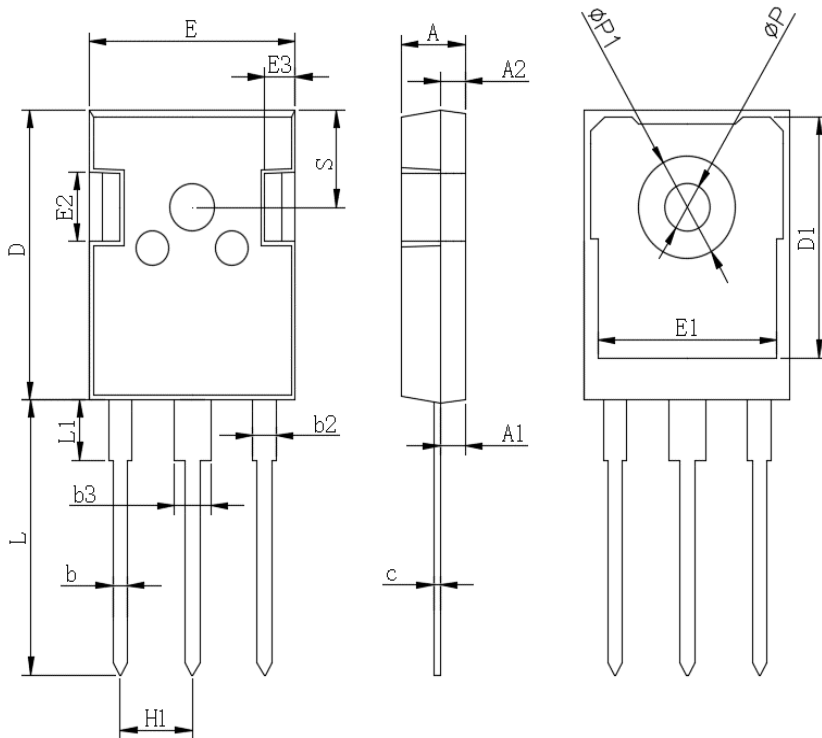


Figure 21. Switching Times Definition



■Outline Dimensions

TO-247AB



TO-247AB		
Dim	Min	Max
A	4.80	5.20
A1	2.21	2.61
A2	1.85	2.15
b	1.0	1.4
b2	1.91	2.21
C	0.5	0.7
D	20.70	21.30
D1	16.25	16.85
E	15.50	16.10
E1	13.0	13.6
E2	4.80	5.20
E3	2.30	2.70
L	19.62	20.22
L1	-	4.30
$\phi P$	3.40	3.80
$\phi P1$	-	7.30
S	6.15TYP	
H1	5.44TYP	
b3	2.80	3.20



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